



CPU1 MEGA128

D0_PA0	51	AD0/PA0	X1	24	Q1	
D1_PA1	50	AD1/PA1		23	16M0	
D2_PA2	49	AD2/PA2	X2	1	VCC	
D3_PA3	48	AD3/PA3		20	/RESET	
D4_PA4	47	AD4/PA4				
D5_PA5	46	AD5/PA5				
D6_PA6	45	AD6/PA6				
D7_PA7	44	AD7/PA7				
					25	I2CCLK
					26	I2CDAT
/RD_PG1	34	PD0/INT0/SCL		27	RXD1	
/WR_PG0	33	RD/PG1 PD1/INT1/SDA		28	TXD1	
ALE_PG2	43	WR/PG0 PD2/INT2/RXD1		29	PD4	
					30	PD5
					31	PD6
					32	PD7
A8_PC0	35	PD4/IC1		2	RXD	
A9_PC1	36	A8/PC0 PD5/XCK1		3	TXD	
A10_PC2	37	A9/PC1 PD6/T1		4	PE2	
A11_PC3	38	A10/PC2 PD7/T2		5	PE3	
A12_PC4	39	A11/PC3		6	PE4	
A13_PC5	40	A12/PC4 RXD/PE0/PDI		7	PE5	
A14_PC6	41	A13/PC5 TXD/PE1/PDO		8	PE6	
A15_PC7	42	A14/PC6 PE2/AC+/XCK0		9	PE7	
					10	/CSMMC
					11	
					12	
					13	
					14	PB4
					15	PB5
					16	PB6
					17	PB7
					18	PG3
					19	PG4
					20	PG3
					21	VCC
					22	VCC
					23	VCC
					24	VCC
					25	VCC
					26	VCC
					27	VCC
					28	VCC
					29	VCC
					30	VCC
					31	VCC
					32	VCC
					33	VCC
					34	VCC
					35	VCC
					36	VCC
					37	VCC
					38	VCC
					39	VCC
					40	VCC
					41	VCC
					42	VCC
					43	VCC
					44	VCC
					45	VCC
					46	VCC
					47	VCC
					48	VCC
					49	VCC
					50	VCC
					51	VCC

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